Docket No.: P2001,0196

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

OLIVER KNIFFLER ET AL.

Filed

CONCURRENTLY HEREWITH

Title

DATA BUS CONFIGURATION HAVING A DATA BUS WHICH CAN BE OPERATED IN MULTIPLEX MODE, AND METHOD

FOR OPERATING THE CONFIGURATION

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

- U.S. Patent No. 5,546,396 (Clauzel et al.), dated August 13, 1996;
- U.S. Patent No. 4,827,471 (Geiger et al.), dated May 2, 1989;
- U.S. Patent No. 4,713,805 (Henaff), dated December 15, 1987;

Färber, G.: "Bussysteme – Parallele und serielle Bussysteme in Theorie und Praxis" [Bus Systems – Parallel and Serial Bus Systems in Theory and in Practice], Oldenbourg Verlag GmbH, 1984, pp. 47-50.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant

Respectfully submitted

For Applicants

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Date: September 15, 2003

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/nt/kf

FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: P2001,0196 Appl. No.: Applicant: OLIVER KNIFFLER ET AL. Filing Date: September 15, 2003 Group Art Unit:				
				Group Art Onit.				
EXAMINER	<u> </u>				1	SUB	FIL	ING
INITIALS	 	PATENT NO.	DATE	PATENTEE	CLASS	CLASS	DA	TE
	A	5,546,396	8/13/96	Clauzel et al.				
	В	4,827,471	5/2/89	Geiger et al.	ļ		 	
	С	4,713,805	12/15/87	Henaff				
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FOREIGN PATENT DOCUMENT								
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRAI YES	
	J					00:100		1,40
	К							
	L							
	М							
	N							
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)								
		Färber, G.: "Bussysteme – Parallele und serielle Bussysteme in Theorie und Praxis" [Bus Systems – Parallel and Serial Bus Systems in Theory and in Practice], Oldenbourg Verlag GmbH, 1984, pp. 47-50						
EXAMINER				DATE CONSIDERED				